

### Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

### Listing of Claims

1-23. (Canceled)

24. (Currently Amended) A display device comprising:

a first substrate having at least one side edge;

a plurality of first conductive lines extending over the first substrate in a first direction;

a plurality of second conductive lines extending over the first substrate in a second direction orthogonal to said first direction;

an interlayer insulating film disposed between said first conductive lines and said second conductive lines;

a plurality of thin film transistors disposed at locations adjacent to intersections of said first conductive lines and said second conductive lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

a second substrate located separated from said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a conductive layer comprising a same material as said plurality of second conductive lines and interposed between said portion of the sealing member and said first substrate; and

a black matrix at least partly overlapped with said conductive layer;

wherein said conductive layer continuously extends along said side edge of said first substrate for more than a pitch of adjacent ones of said second conductive lines, and is electrically isolated from both of said plurality of first conductive lines and said plurality of second conductive lines.

25. (Previously Presented) The display device according to claim 24 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

26. (Previously Presented) The display device according to claim 24 wherein each channel region of each of said plurality of thin film transistors has a crystalline structure.

27. (Canceled)

28. (Previously Presented) The display device according to claim 24 wherein said conductive layer extends in a form of a rectangular wave.

29. (Currently Amended) A display device comprising:

a first substrate having at least one side edge;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said thin film transistors;

an interlayer insulating film disposed between said scanning lines and said signal lines;

a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a conductive layer interposed between said portion of the sealing member and said first substrate, said conductive layer comprising a same material as said plurality of scanning lines[.]; and

a black matrix at least partly overlapped with said conductive layer;

wherein said conductive layer continuously extends along said side edge of said first substrate for more than a pitch of adjacent ones of said scanning lines, and is electrically isolated from both of said plurality of scanning lines and said plurality of signal lines.

30. (Previously Presented) The display device according to claim 29 wherein each of said plurality of thin film transistors is a top-gate type thin film transistor.

31. (Previously Presented) The display device according to claim 29 wherein each channel region of said plurality of thin film transistors has a crystalline structure.

32. (Canceled)

33. (Previously Presented) The display device according to claim 29 wherein said conductive layer extends in a form of a rectangular wave.

34-40. (Canceled)

41. (Currently Amended) A display device comprising:

- a first substrate having at least one side edge;

- a plurality of first conductive lines extending over the first substrate in a first direction;

- a plurality of second conductive lines extending over the first substrate in a second direction orthogonal to said first direction;

- a plurality of first thin film transistors disposed at each intersection of said first conductive lines and said second conductive lines;

- a plurality of pixel electrodes electrically connected to said first thin film transistors;

- an interlayer insulating film disposed between said first conductive lines and said second conductive lines;

- a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member; and

a conductive layer comprising a same material as said plurality of second conductive lines and interposed between said portion of the sealing member and said first substrate; and a black matrix at least partly overlapped with said conductive layer;

wherein said conductive layer continuously extends along said side edge of said first substrate for more than a pitch of adjacent ones of said second conductive lines, and is electrically isolated from both of said plurality of first conductive lines and said plurality of second conductive lines.

42. (Previously Presented) The display device according to claim 41 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

43. (Previously Presented) The display device according to claim 41 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

44. (Canceled)

45. (Previously Presented) The display device according to claim 41 wherein said conductive layer extends in a form of a rectangular wave.

46. (Currently Amended) A display device comprising:

a first substrate having at least one side edge;

a plurality of scanning lines extending over the first substrate in a first direction;

a plurality of signal lines extending over the first substrate in a second direction;

a plurality of first thin film transistors disposed at each intersection of said scanning lines and said signal lines;

a plurality of pixel electrodes electrically connected to said first thin film transistors;  
an interlayer insulating film disposed between said scanning lines and said signal lines;  
a second substrate opposed to said first substrate;

a sealing member disposed at a periphery of said first and second substrates, said sealing member having a portion adjacent to said side edge;

a driver circuit comprising at least one second thin film transistor formed over said first substrate, said driver circuit disposed within a region surrounded by said sealing member;

a conductive layer interposed between said portion of the sealing member and said first substrate, said conductive layer comprising a same material as said plurality of scanning lines[.]; and

a black matrix at least partly overlapped with said conductive layer;

wherein said conductive layer continuously extends along said side edge of said first substrate for more than a pitch of adjacent ones of said scanning lines, and is electrically isolated from both of said plurality of scanning lines and said plurality of signal lines.

47. (Previously Presented) The display device according to claim 46 wherein each of said plurality of first thin film transistors is a top-gate type thin film transistor.

48. (Previously Presented) The display device according to claim 46 wherein each channel region of said plurality of first thin film transistors has a crystalline structure.

49. (Canceled)

50. (Allowed) The display device according to claim 46 wherein said conductive layer extends in a form of a rectangular wave.

51-72. (Canceled)

73. (New) A display device according to claim 24 wherein the black matrix is located between the sealing member and the conductive layer.

74. (New) A display device according to claim 29 wherein the black matrix is located between the sealing member and the conductive layer.

75. (New) A display device according to claim 41 wherein the black matrix is located between the sealing member and the conductive layer.

76. (New) A display device according to claim 46 wherein the black matrix is located between the sealing member and the conductive layer.